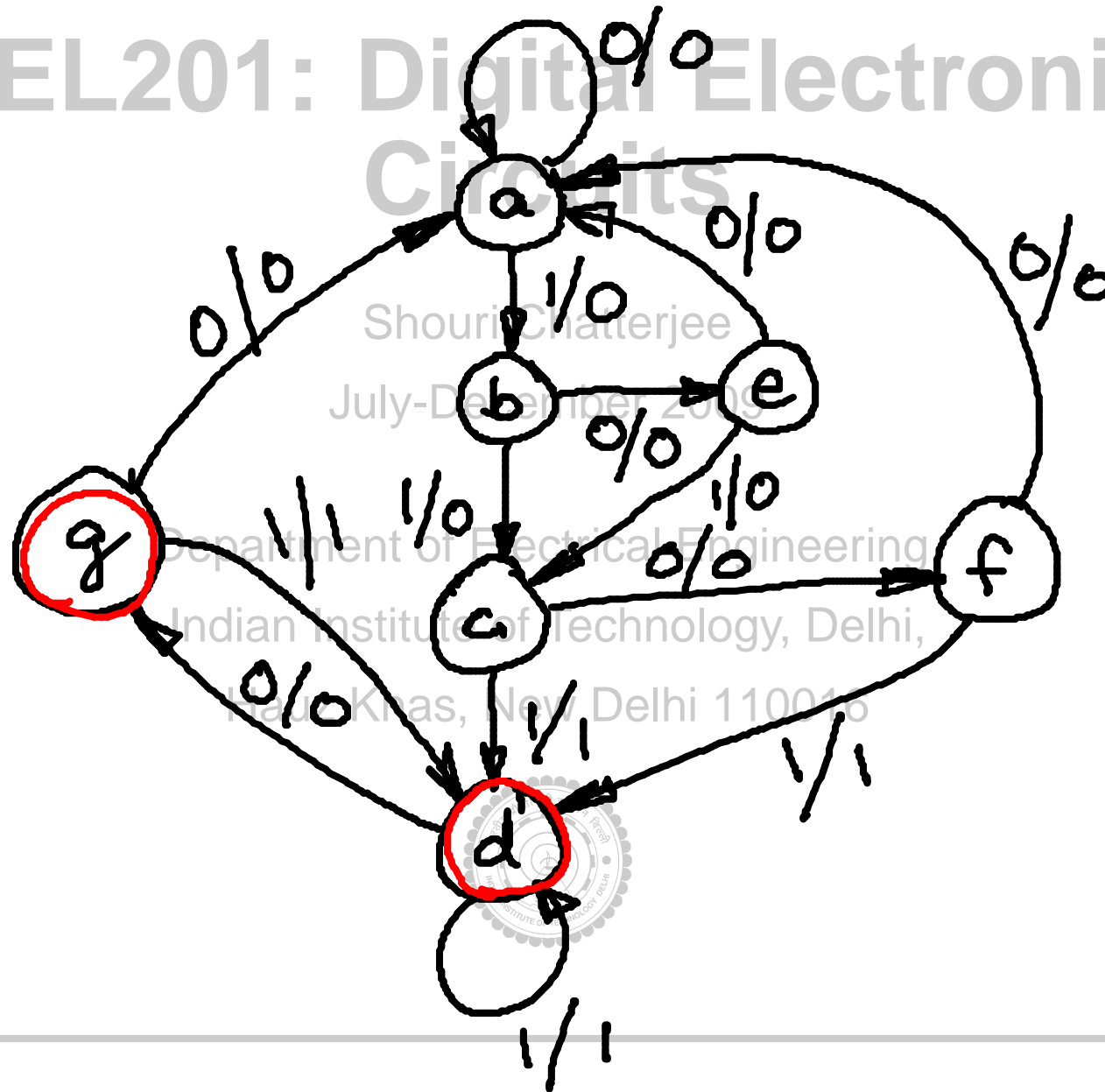


# State reduction



Current state	Input	Next state	Output
a	0	a	0
	1	b	0
b	0	c	0
	1	d	0
c	0	e	0
	1	f	1

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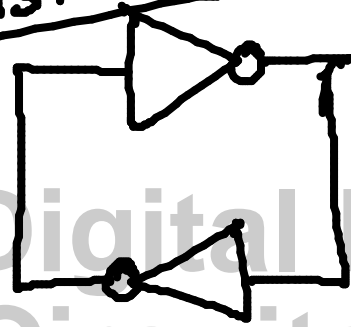
# Memory 0. registers → flip-flops (D)

1. SRAM

static

2. DRAM

dynamic



3. ROM

read only

4. Magnetic (tape, floppy disks, hard disks)

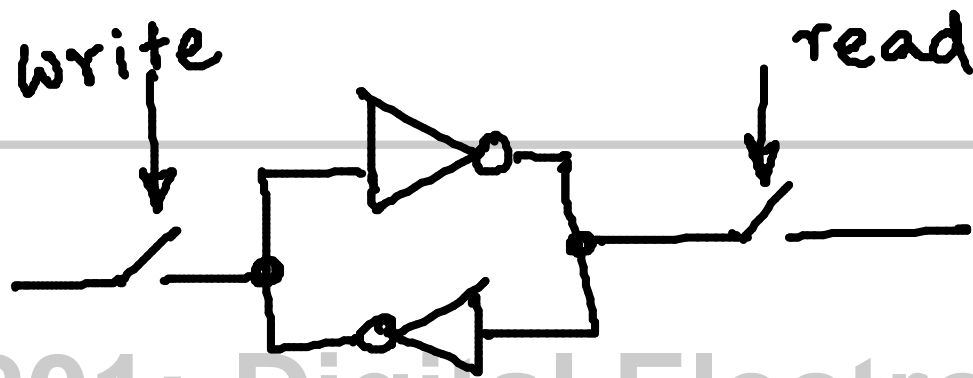
5. Optical (CD-ROMs)

6. Flash

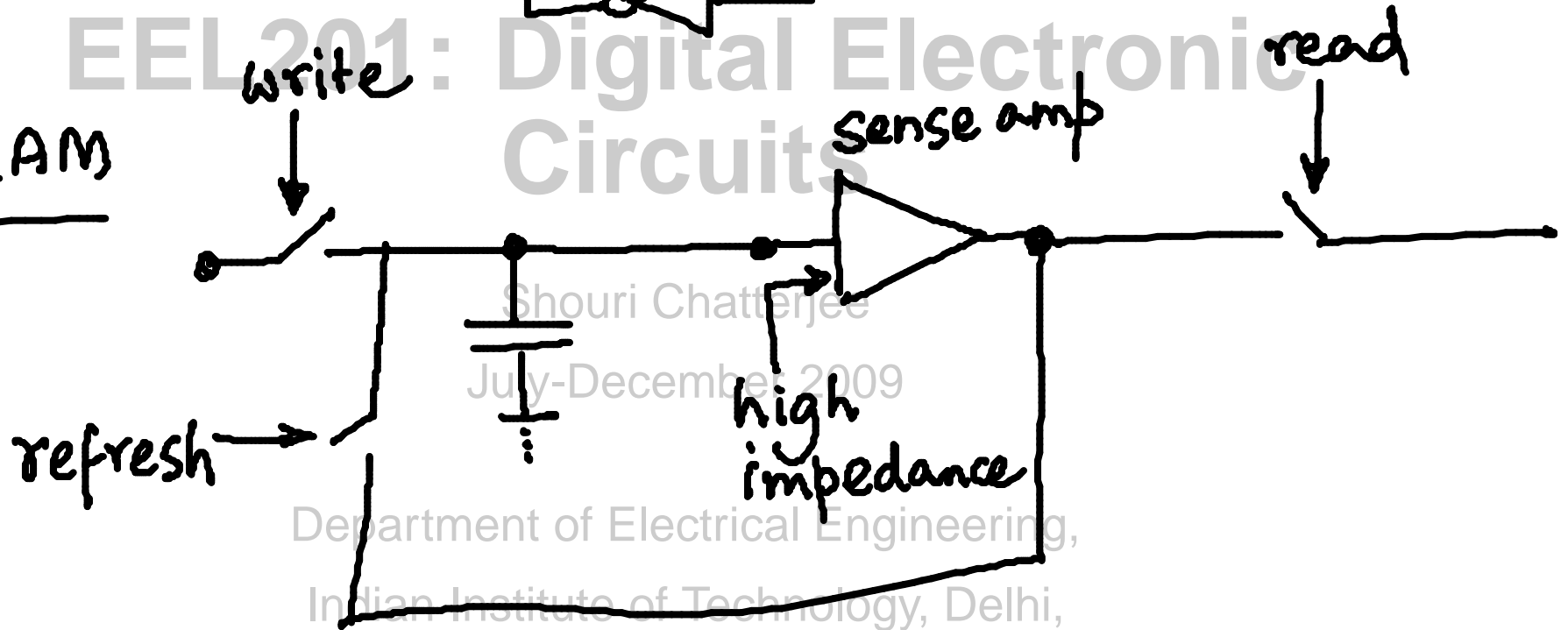
[ NOR  
NAND



# 1. SRAM



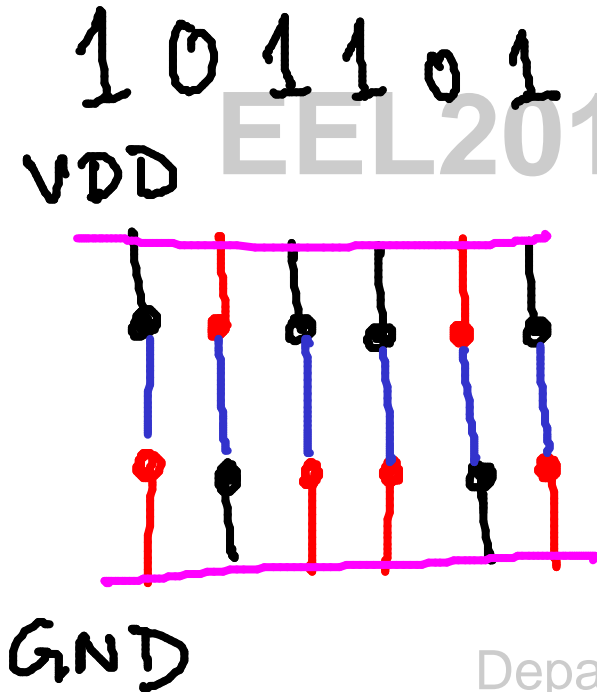
# 2. DRAM



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### 3. Read-only



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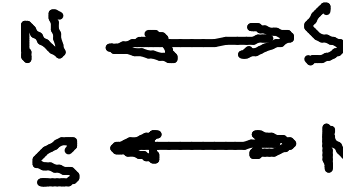
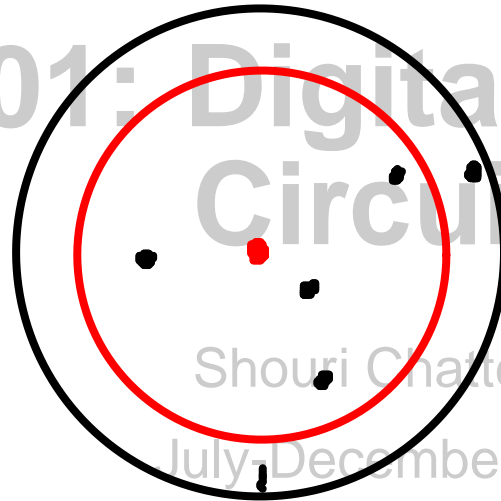
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# EEL201: Digital Electronic Circuits

120 Hz  
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