

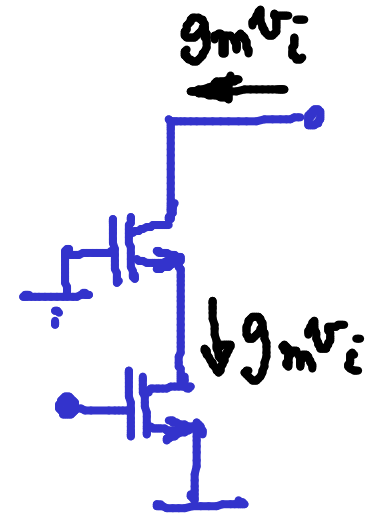
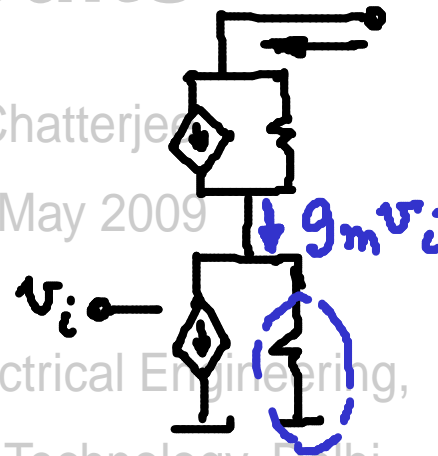
# EEL 782: Analog Integrated Circuits

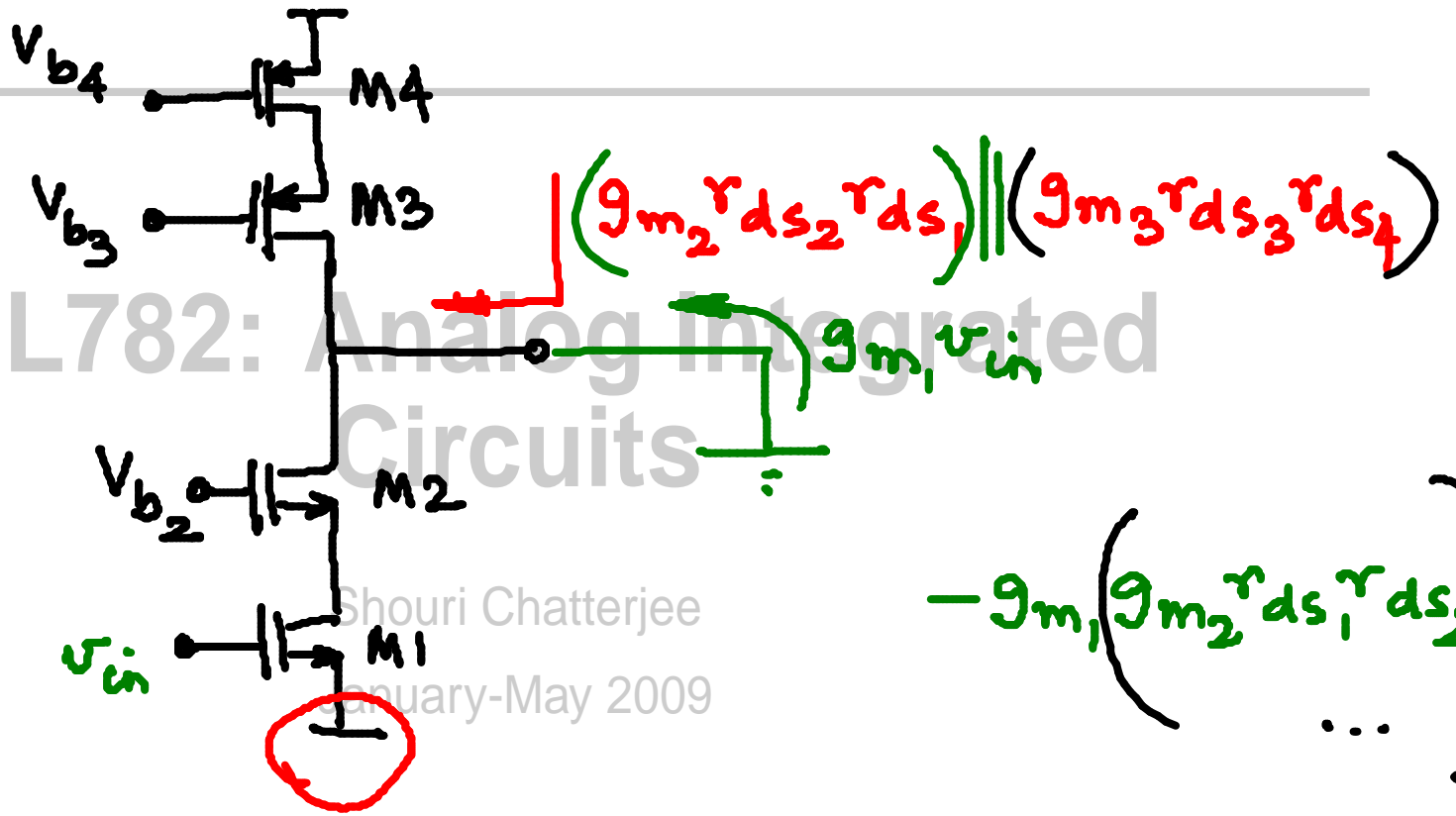
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January-May 2009

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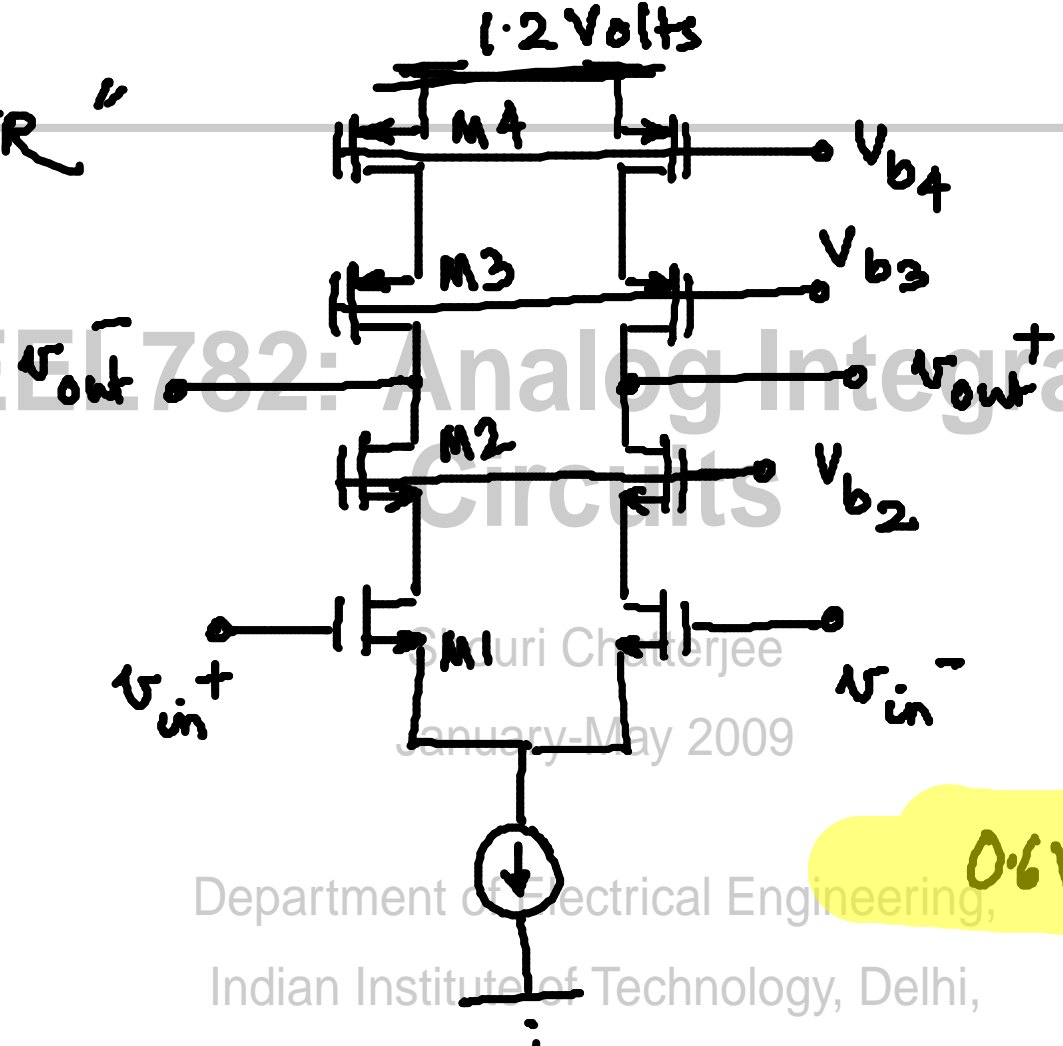
EEL782: Analog Integrated Circuits

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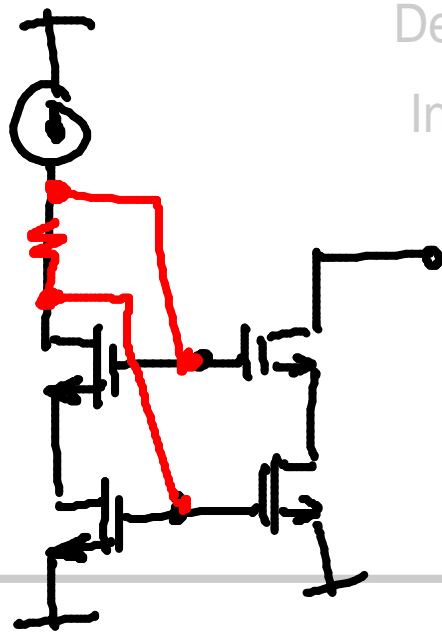
# "CASCODE AMPLIFIER"



Output swing

$$0.6V < V_{out} < 0.8V$$

(+ or -)



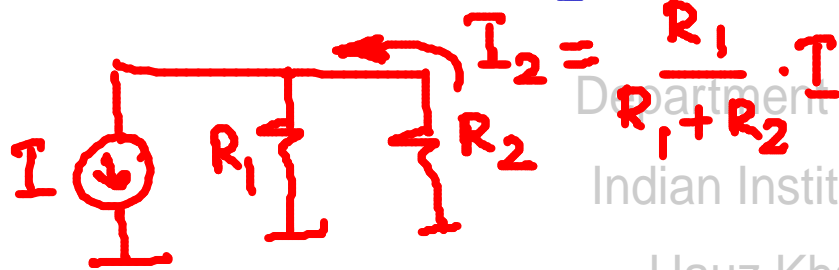
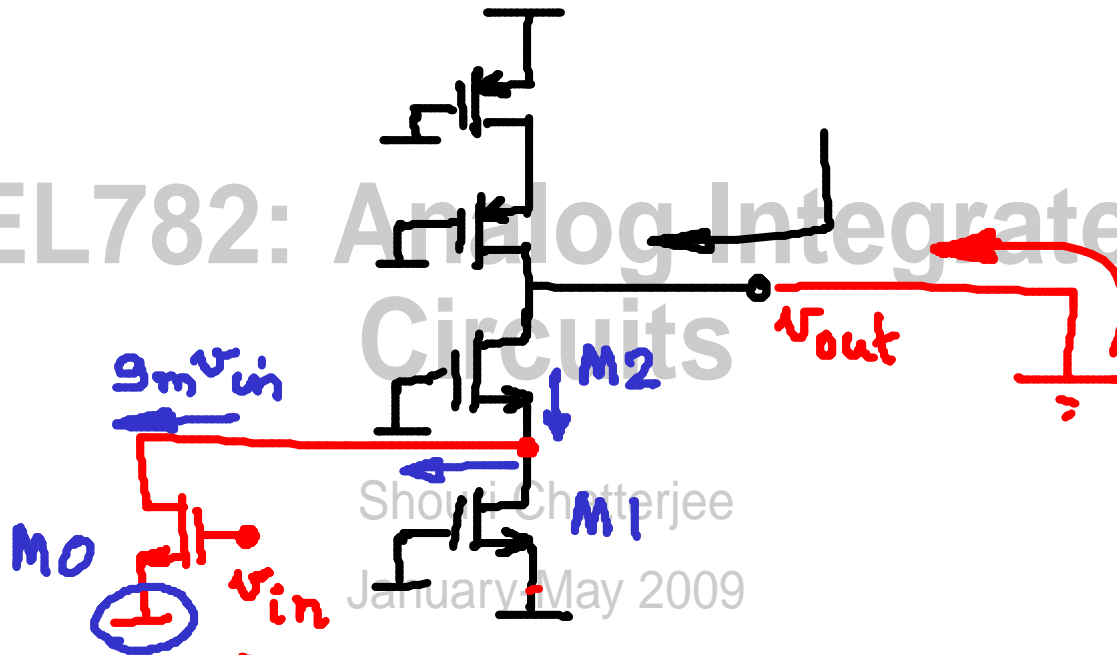
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# "FOLDED CASCODE AMPLIFIER"

EEL782: Analog Integrated Circuits

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$$\frac{g_{m_0} v_{in}}{(r_{ds_0} \parallel r_{ds_1}) + \left(\frac{1}{g_{m_2}} \parallel r_{ds_2}\right)}$$

